Accelerate Your HPC and Al Workloads



The Intel® Server D50DNP Family delivers breakthrough HPC and Al performance across the platform

High-performance processing



4/5th Gen Intel® Xeon® Scalable processors





Intel® Xeon® CPU Max Series Integrated High Bandwidth Memory (HBM) delivers up to 1TB bandwidth



Built-in **Al acceleration** for deep learning training and inferencing



In-memory analytics acceleration built-in

Ultra-fast memory and I/O to accelerate data movement





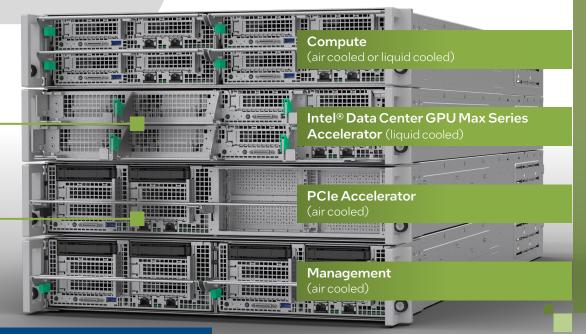


High-density support for the latest HPC and Al accelerators

- Purpose-built for data center workloads
- Supports up to four Intel GPUs in a liquidcooled IU module (eight per 2U chassis)



- High-density PCIe accelerator module
- Supports up to four full-height, full-length, double-width PCIe 5.0 accelerators



Learn more on the web: datacentersolutions, mitacmct.com/Intel-Server-D50DNP-Family

 $1. DDR5 \, memory for 1.5x \, memory \, bandwidth \, versus \, DDR4 \, memory \, compares \, 4th \, Gen \, Intel \, excessor \, with \, 8 \, channels \, of \, DDR5 \, at \, up \, to \, 4800 \, MT/s \, for \, 1 \, DIMM \, per \, channel \, (1 \, DPC) \, vs. \, 3rd \, Gen \, Intel \, excessor \, with \, 8 \, channels \, of \, DDR4 \, at \, 3200 \, MT/s \, for \, 2 \, DIMMs \, per \, channel \, (2 \, DPC).$ $2. \, 2x \, I/O \, performance \, versus \, previous \, generation \, compares \, PCIe \, 5.0 \, at \, 32 \, GT/s \, transfer \, rate \, vs. \, PCIe \, 4.0 \, at \, 16 \, GT/s.$

Performance varies by use, configuration, and other factors. Your costs and results may vary. Learn more at www.lntel.com/PerformanceIndex.lntel technologies may require enabled hardware, software, or service activation. No product or component can be absolutely secure. All product plans and roadmaps are subject to change without notice. Code names are used by Intel to identify products, technologies, or services that are in development and not publicly available. These are not "commercicial" names and not intended to function as trademarks.

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